Customer No.: 31561
Application No.: 10/708,805
Docket No.: 12264-US-PA

AMENDMENTS

Amendments to the Claims:

This listing of claims will replace all prior versions and listings of claims in the application:

Listing of Claims:

Claim 1 (currently amended) A system for accessing a plurality of devices using a single bus, comprising:

a first device;

a second device;

a shared bus, coupled to the first device;

a bus isolator, coupled to the shared bus and the second [[bus]] device for isolating the second device from the shared bus or connecting the second device to the shared bus; and

a control apparatus coupled to the shared bus so that the bus isolator isolates the second device from the shared bus when the control apparatus needs to access the first device and the bus isolator connects the second device with the shared bus when the control apparatus needs to access the second device.

Claim 2 (original) The system of claim 1, wherein the control apparatus further comprises:

a bus exchanger, coupled to the shared bus for switching the authority for the shared bus between different devices; and

a bus arbitrator, coupled to the bus exchanger so that the bus arbitrator controls the bus exchanger to connect the shared bus with a circuit internally linked to the first device when the control apparatus needs to access the first device and the bus arbitrator controls the bus exchanger to connect the shared bus with a circuit internally linked to the second device when the control apparatus needs to access the second device.

Claim 3 (original) The system of claim 2, wherein a pre-defined isolation period must pass before the bus exchanger is permitted to switch the device for authority for the shared bus.

Claim 4 (original) The system of claim 1, wherein the second device comprises a memory card compatible device.

Claim 5 (original) The system of claim 4, wherein the memory card compatible device is either a memory card or a card reader.

Claim 6 (original) The system of claim 1, wherein the first device comprises a memory device.

Claim 7 (currently amended) A control apparatus for accessing a plurality of devices through a single bus, the control apparatus connects to a first device through a shared bus and the control apparatus also connects to a second device through the shared bus and a bus isolator, the control apparatus comprising:

a bus exchanger, coupled to the shared bus for switching the authority of device for the shared bus; and

~~temptompa~~ a bus arbitrator coupled to the bus exchanger such that the bus arbitrator controls the bus exchanger to connect with a circuit internally linked to the first device and to activate the bus isolator to isolate the second device from the shared bus when the control apparatus needs to access the first device and the bus arbitrator controls the bus exchanger to connect with a circuit internally linked related to the second device when the control apparatus needs to access the first device.

Claim 8 (original) The control apparatus of claim 7, wherein the bus exchanger is set to wait for the passage of a pre-defined isolation period lasting from the end of accessing the first device to the start of accessing the second device before switching the control of the shared bus from the first device to the second device.

Claim 9 (original) The control apparatus of claim 7, wherein the second device comprises a memory compatible device.

Claim 10 (original) The control apparatus of claim 7, wherein the memory compatible device is either a memory card or a card reader.

Claim 11 (original) The control apparatus of claim 7, wherein the first device comprises a memory unit.

Claim 12 (currently amended) A system for accessing a plurality of devices through a single bus, comprising:

a memory unit;

a memory card compatible device;

a shared bus, coupled to the memory unit; and

a control apparatus coupled to the shared bus such that the control apparatus controls the shared bus to connect with a circuit internally linked to the ~~first device~~ memory unit when the control apparatus needs to access the ~~first device~~ memory unit and the control apparatus controls the shared bus to connect with a circuit internally linked to the ~~second device~~ memory card compatible device when the control apparatus needs to access the ~~second device~~ memory card compatible device.

Claim 13 (currently amended) The system of claim 12, wherein a pre-defined isolation period must pass before the control apparatus is permitted to access the ~~second device~~ memory card compatible device through the shared bus.

Claim 14 (original) The system of claim 12, wherein the memory card compatible device is either a memory card or a card reader.

Claim 15 (original) The system of claim 12, wherein the memory unit comprises read-only memory.